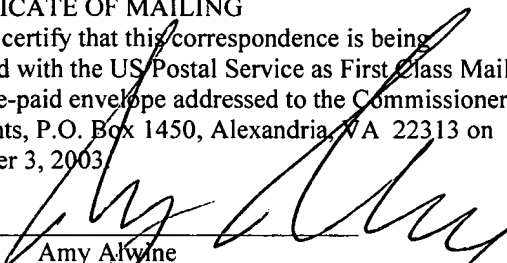


1742
PATENT *Image*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: TALIEH et al. Serial No.: 09/941,360 Filed: August 28, 2001 Title: Method And Apparatus For Plating and Polishing A Semiconductor Substrate	Group Art Unit: 1742 Examiner: D. Valentine Docket: NT-004D-US CERTIFICATE OF MAILING I hereby certify that this correspondence is being deposited with the US Postal Service as First Class Mail in a postage-paid envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313 on December 3, 2003. Signed:  Amy Alwine
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RESPONSE TO NOTICE OF NON-COMPLIANT AMENDMENT

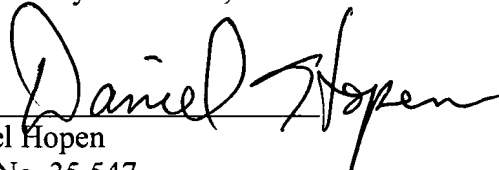
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313

Dear Sir:

This is a response to a Notice of Non-Compliant Amendment dated November 11, 2003 indicating that a complete listing of all of the claims is not present, and all claims must be submitted with their current status indicators. Applicants' record indicates that all pending claims were presented with their current status indicators.

As indicated in the Office Action mailed July 2, 2003, only claims 20-27 and 30-33 are pending in the application. Accordingly, claims 20-27 and 30-33 were submitted with their current status indicators. Attached is a copy of the entire listing of the claims consistent with the listing of claims which were presented in Applicants' amendment of October 22, 2003.

Respectfully submitted,

By: 
Daniel Hopen
Reg. No. 35,547

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